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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Akio NISHIDA, et al.  
Serial No.: 10/781,861  
Filed: February 20, 2004  
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND A  
METHOD OF MANUFACTURING THE SAME  
Group: 2815  
Examiner: WARREN, Matthew E.  
Confirmation No.: 4775

**AMENDMENT / REPONSE TO ELECTION OF SPECIES REQUIREMENT**

**Mail Stop: AMENDMENT**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

March 2, 2006

Sir:

In response to the Office Action dated February 9, 2006, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments** to the claims begin on page 2;

**Remarks** are included beginning on page 13; and

**Authorization** is included on page 14.